FROM STU AUVINEN 831 477 0703

P. 1

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Date: 4/15/02

Number of pages including cover: 3

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REMARKS:	Urgent	For your review	Reply ASAP	☐ Please comment
Ser. No. 09/683	3,968 Group	2836		
INVENTION D	ISCLOSURE ST	ATEMENT		
Attached is a cop	y of an IDS that	was mailed on the da	te shown on the cert	ificate of mailing.
Please place this	copy in the file.	The Examiner is aske	ed to review these re	ferences or call the
undersigned if w	nable to consider	the references for any	y reason.	
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Stuart T. Auvine	en			APR 15 2002
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hereby certify that this paper is being deposited with the United States Poetal Service with ufficient poetage as First Citas Meil under S7 CFR 1.8 on the date indicated below and is donessed to "Communication of Paterna and Trademarks, Weshington, DC 20231."
Stuart T. Auviners Reg. No. 38,436
lete Signed and Maried 4-15-02

PATENT PS-70

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of)
Kwong et al.) Examiner:
Serial No. 09/683,968	Group Art Unit: 2836
Filed: 3/7/02)
For: ESD-Isolation Circuit Driving Gate of Bus- Switch Transistor During ESD Pulse Between Two I/O Pins))))

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks Washington, DC 20231

Sir:

Attached is a completed Substitute Form PTO-1449 and copies of the pertinent parts of the references cited thereon. This paper is being filed within 3 months of filing, so no fee is due.

The Examiner is invited to review prior art cited in Applicant's related ESD cases Ser. No. 09/607,458 and 09/607,460 that are assigned to GAU 2800.

The commissioner is hereby authorized to charge payment of any patent application processing fees under 37 C.F.R. § 1.17 associated with this communication or credit any overpayment to deposit account No. 01-2950.

Respectfully Submitted,

Stuart T. Auvinen Agent for Applicant Reg. No. 36,435



	Form PTO-1449									Docket Num	PS-70		Applic	et 1 of
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